

FIG. 1
STARLINK BLOCK DIAGRAM

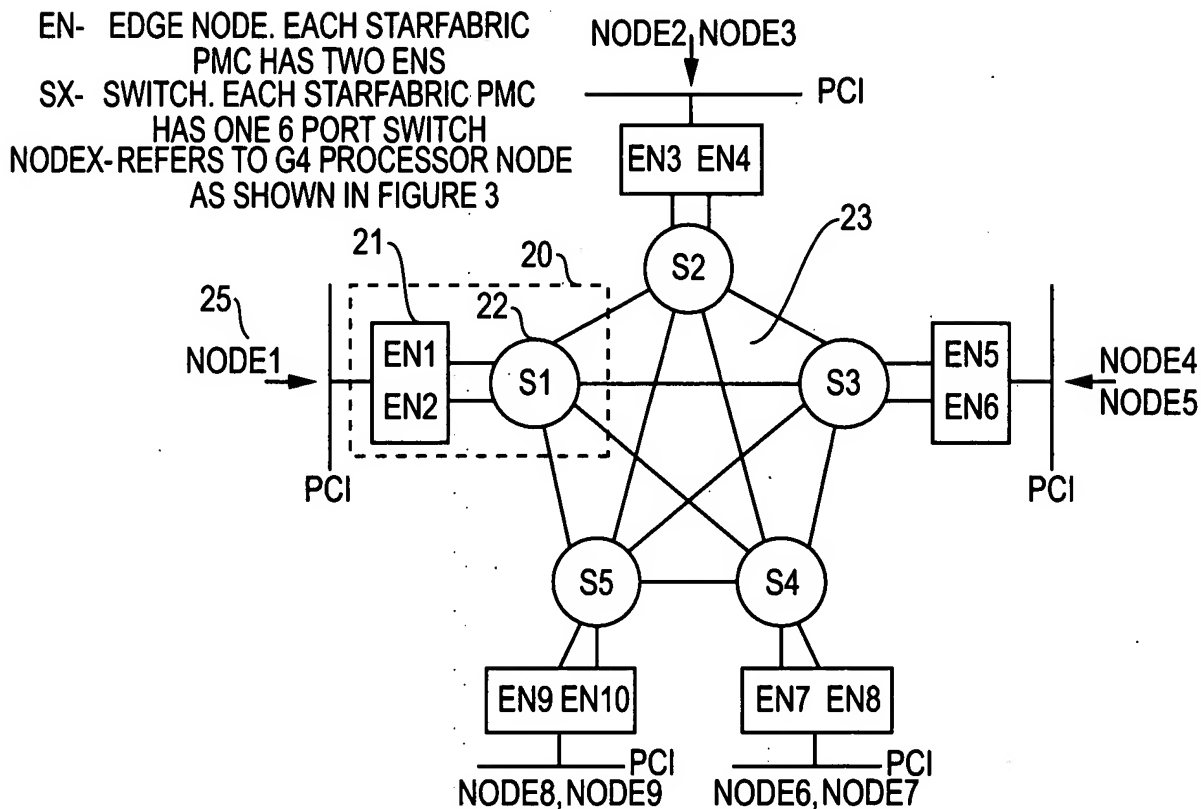


FIG. 2
 APPLICATION EXAMPLE: NETWORK TOPOLOGY

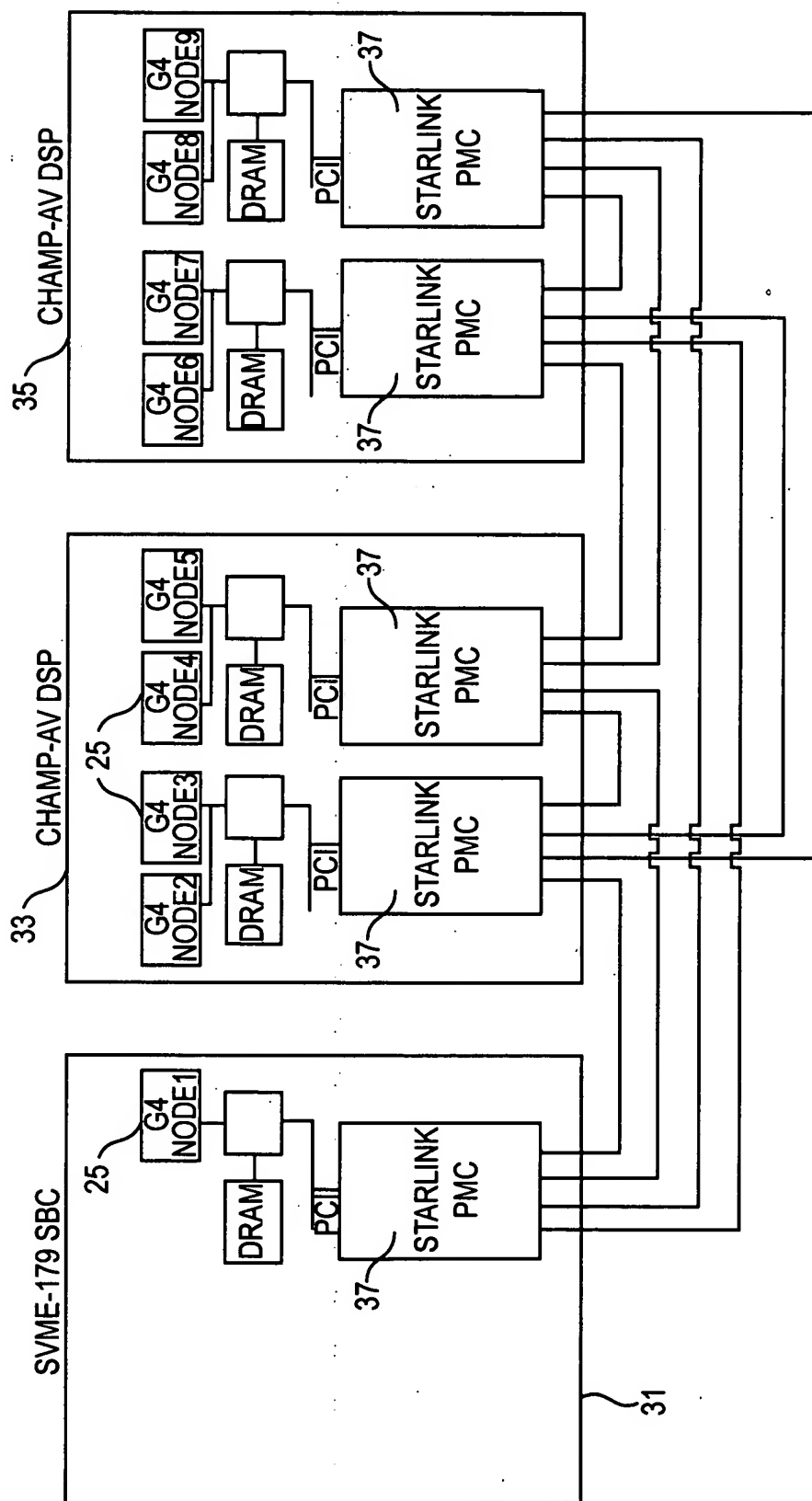


FIG. 3
 APPLICATION EXAMPLE: SYSTEM BLOCK DIAGRAM

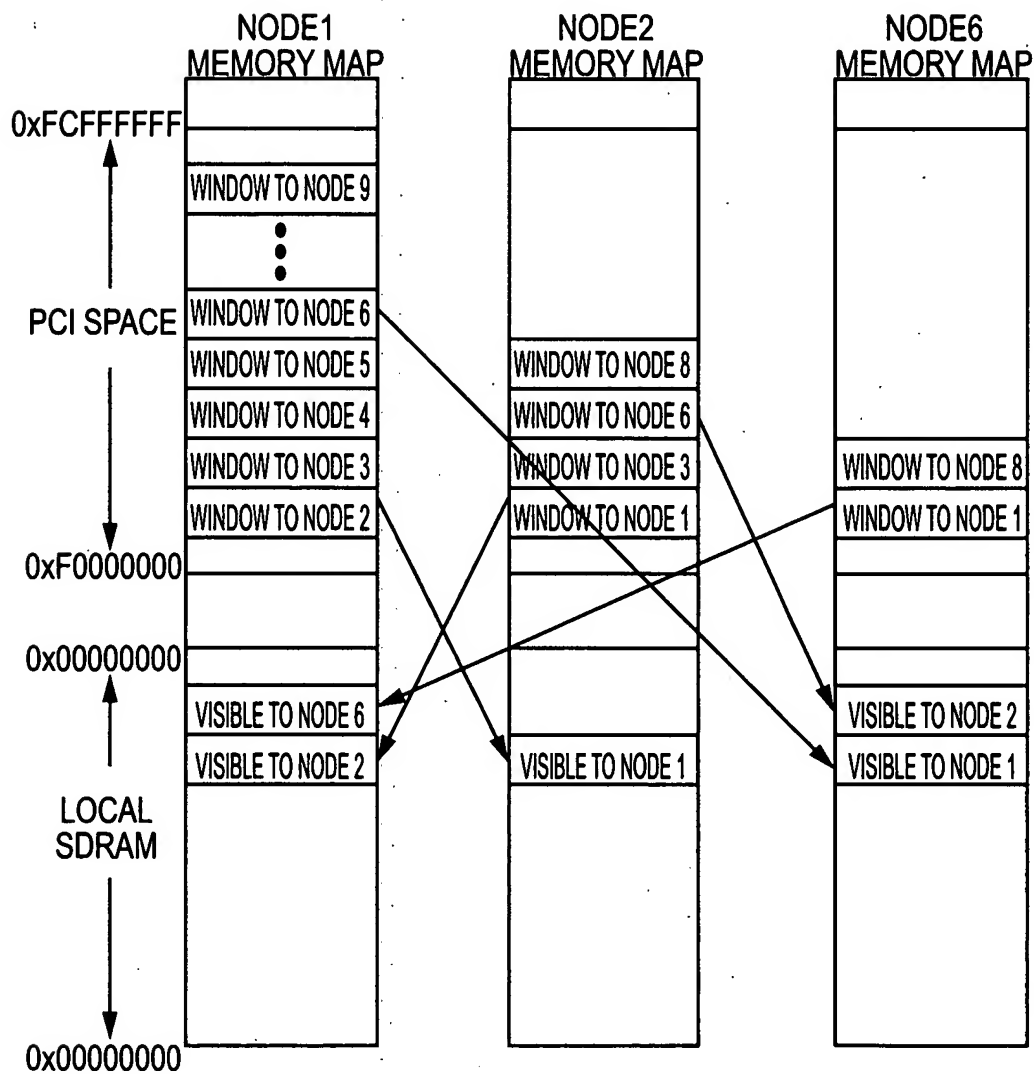


FIG. 4
APPLICATION EXAMPLE:SIMPLIFIED MEMORY MAP

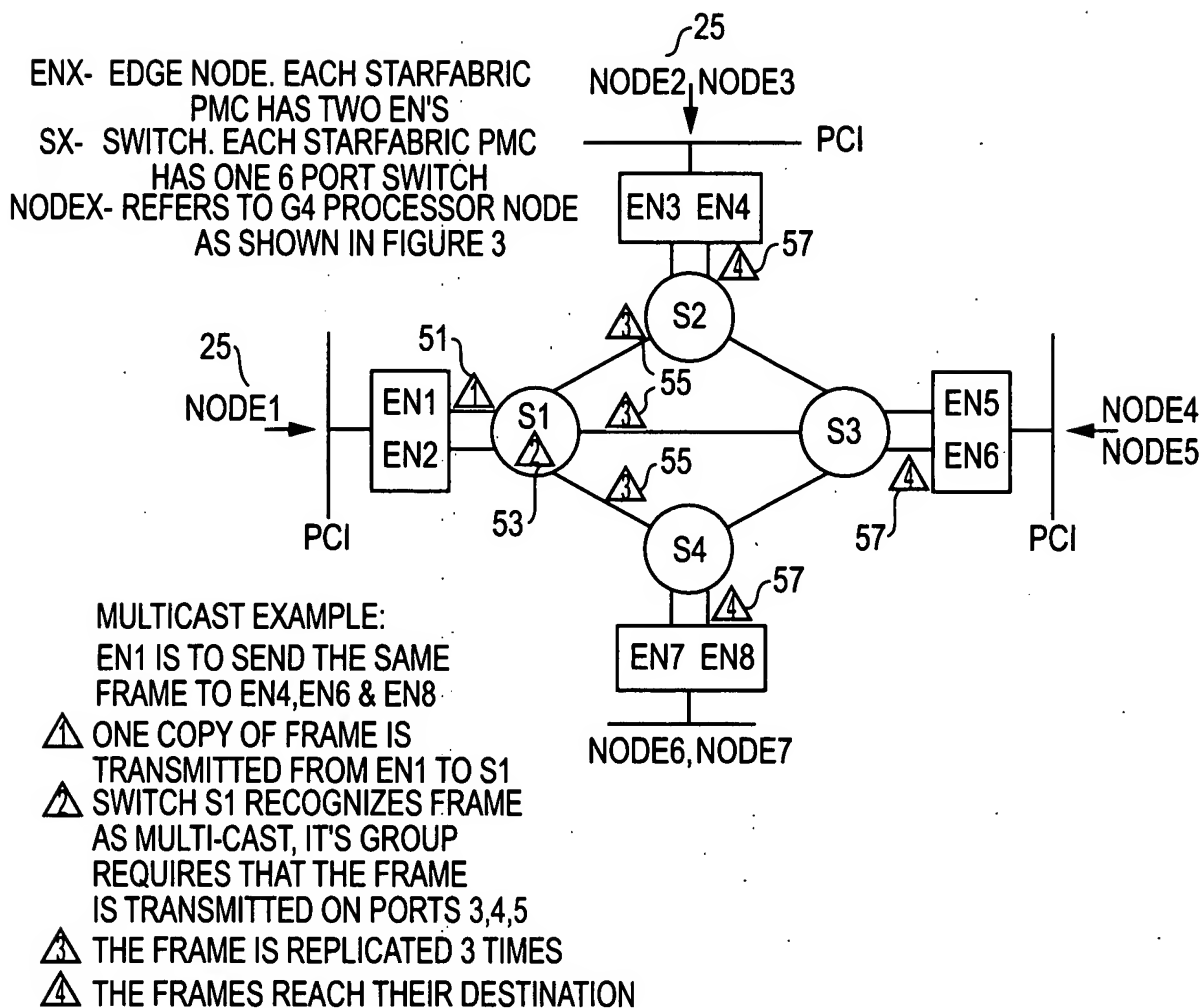


FIG. 5
 MULTICAST EXAMPLE

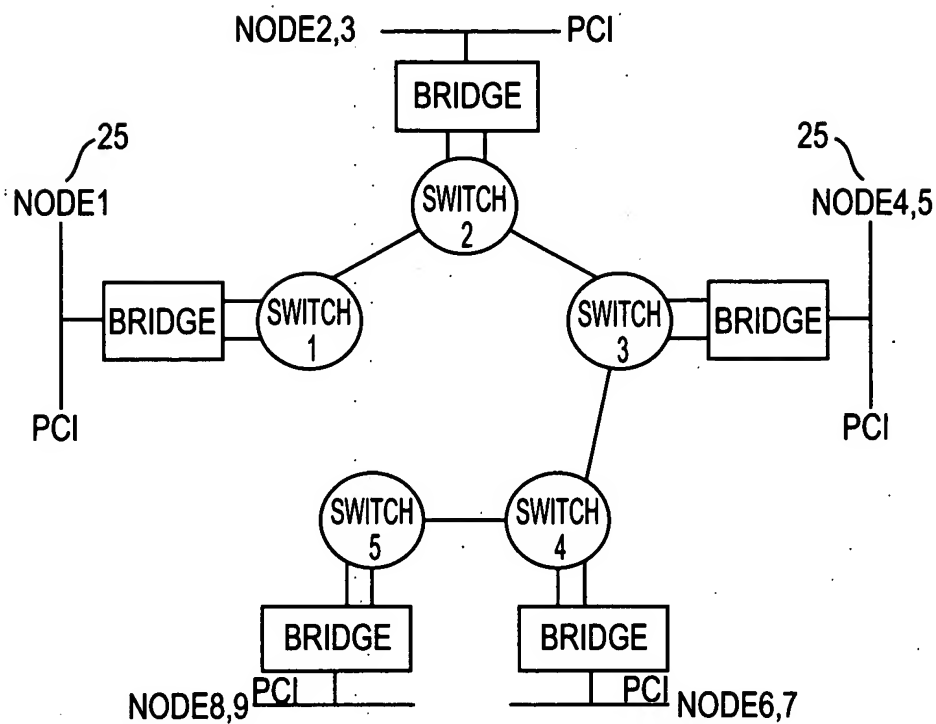


FIG. 6

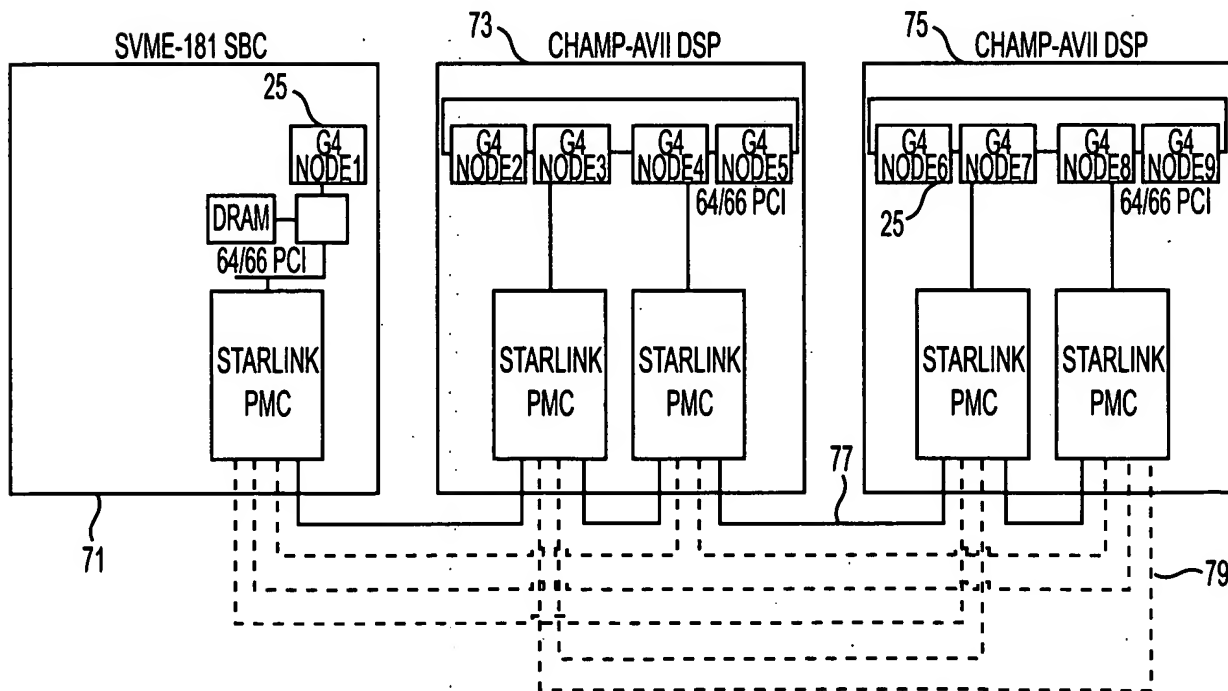
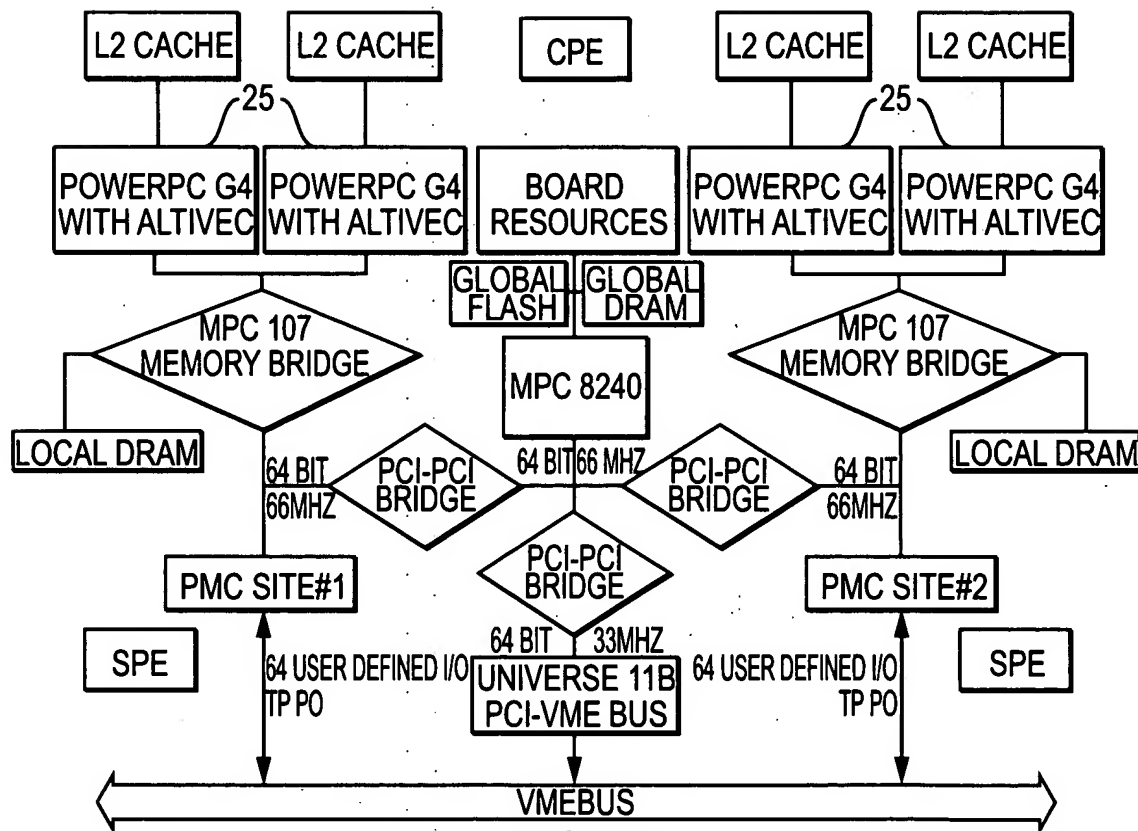


FIG. 7



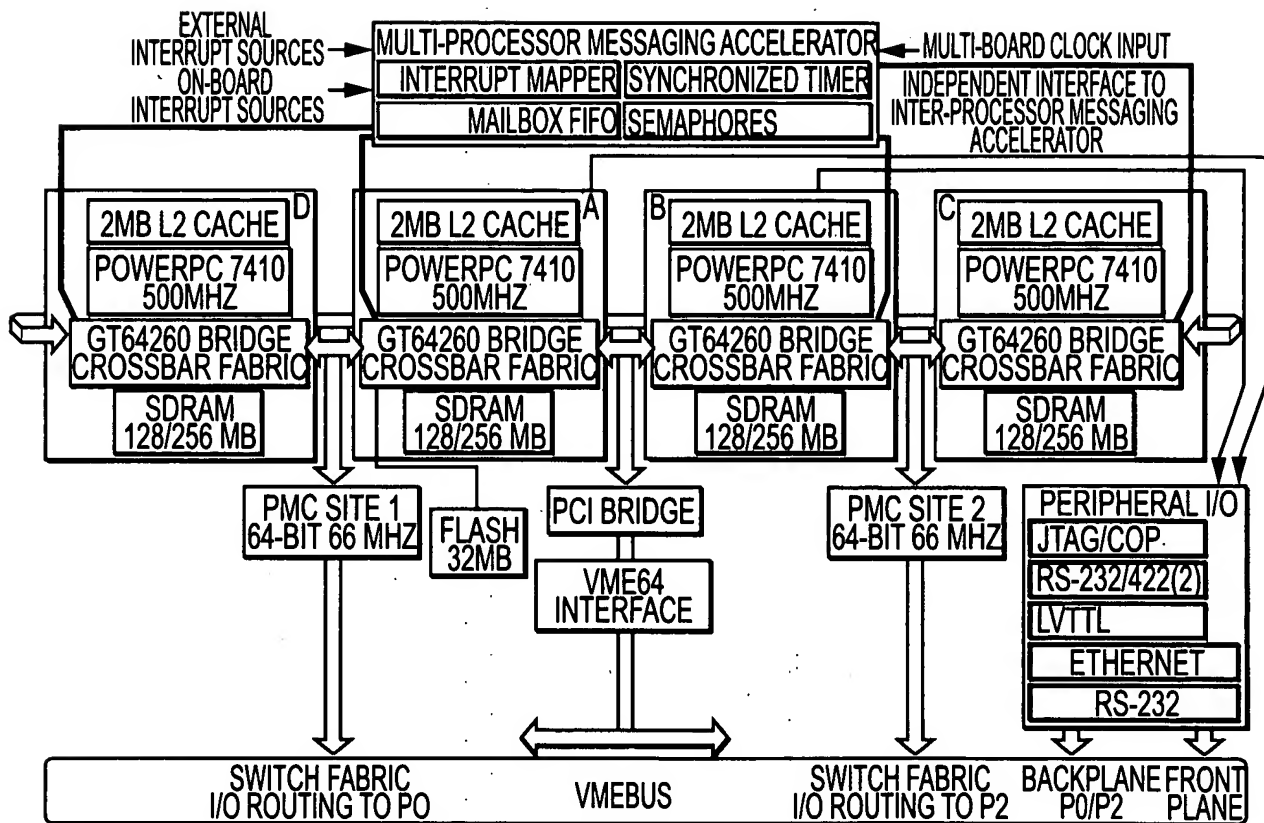


FIG. 9

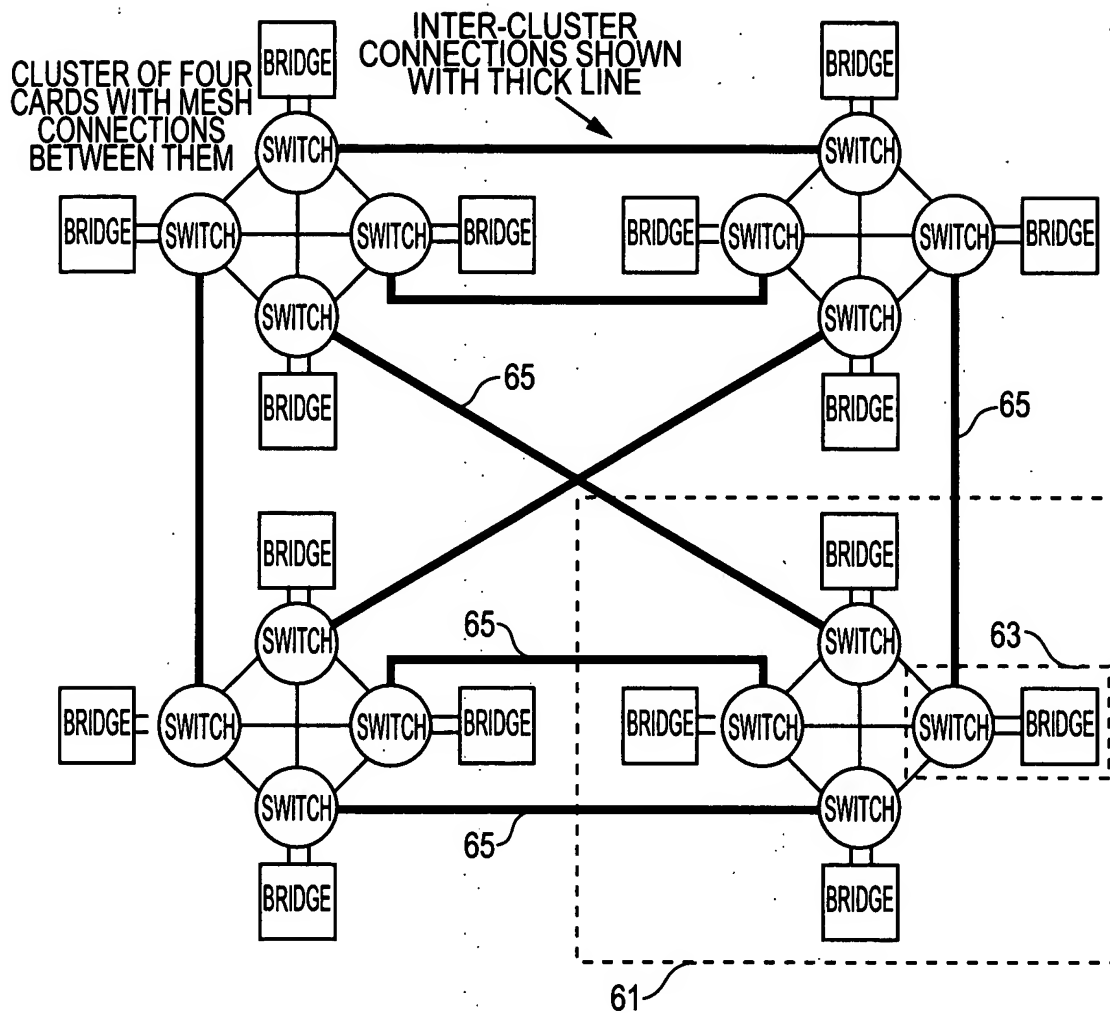


FIG. 10